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10/650,597	08/28/2003	Robert Neal Zettwoch	BOI - 0078US	7935

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ART UNIT	PAPER NUMBER
2416	

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/650,597	ZETTWOCH, ROBERT NEAL	
	Examiner	Art Unit	
	PAO SINKANTARAKORN	2416	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *21 July 2008*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-77* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-77* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 7/21/2008 has been entered.

2. Claims 1-77 are currently pending in the application.

Claim Rejections - 35 USC § 112

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

> The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claims 4, 23, 39, and 62 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claim 4 line 4, the recitation "the transmission rate of the frame decreases to a predetermined address" is vague and indefinite because it is not known how the transmission rate is able to decrease to an address. The same is true for claims 23, 39, and 62.

Claim Rejections - 35 USC § 103

5. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

6. The factual inquiries set forth in *Graham* **v.** *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

8. Claims 1, 2, 5-10, 16-18, 20, 21, 24-28, 34-37, 40-45, and 51-53 are rejected under 35 U.S.C. 103(a) as being unpatentable over Borland (US 6,343,217) and

Lockridge et al. (US 2004/0010729) in view of Watkins et al. (Newly Cited US 6,507,672).

Regarding claim 1, Borland discloses a system for interfacing with at least one node in a Fibre Channel network (see column 3 lines 34-37, the units communicate through a wireless link such as optical signal), the system comprising:

at least one input interface couplable to receive a plurality of frames of data (see Fig 2 reference numeral 225T, the selector 225T is configured to receive an optical signal to be processed by the transmitter, wherein the optical signal could be a plurality of data frames), the frames of data being at least one of transmitted and received at a node of a Fibre Channel network (see Fig 2 reference numeral 225T, the optical signal received by the selector 225T is transmitted from either a microphone 210T or modem port 212); and

an output interface couplable to provide the received frames of data to a device (see column 5 lines 30-32, the TX output stage transmit optical signals to base unit transceiver 120 through wireless PCM link).

Borland fails to disclose a system, wherein at least one of the input interface and the output interface is further configured to time tag the received plurality of frames of data prior to the providing of the frames of data to the device. However, Lockridge et al. from the same or similar fields of endeavor disclose a system comprising timestamping element for placing a time stamp into the data stream when the data is received from a network, the time stamp and the packet are stored in a buffer prior to being provided to other devices (see paragraphs 27 and 38).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device.

The motivation for implementing a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device is that it enables synchronization of the system.

Borland and Lockridge et al. fail to disclose a system, wherein the device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network. Watkins et al. from the same or similar fields of endeavor disclose a device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network (see Figure 3 numerals 316, 318, and 320, column 8 lines 19-24, and column 12 lines 1-3, storing a signal on a recordable multimedia disk in the Fibre Channel network).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement the device includes the recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network as taught by Watkins et al. into the system of Borland and Lockridge et al. in order to increase the reliability of the network.

Regarding claim 20, Borland discloses a system for interfacing with at least one node in a Fibre Channel network, the system comprising:

at least one input interface couplable to receive a plurality of frames of data (see Fig 2 reference numeral 225T, the selector 225T is configured to receive an optical

signal to be processed by the transmitter, wherein the optical signal could be a plurality of data frames), the plurality of frames of data being at least one of transmitted from and received back in a node of a Fibre Channel network (see Fig 2 reference numeral 225T, the optical signal received by the selector 225T is transmitted from either a microphone 210T or modem port 212);

an output interface couplable to provide the received frames of data in pulse code modulation (PCM) formatted frames to a device (see column 5 lines 30-32, the TX output stage transmit optical signals to base unit transceiver 120 through wireless PCM link); and

a processor coupled to control the input interface and the output interface (see Fig 2 reference numeral 225T and column 5 lines 46-53, the selector 225T switches the input between microphone and modem port).

Borland fails to disclose a system, wherein at least one of the input interface and the output interface is further configured to time tag the received plurality of frames of data prior to the providing of the frames of data to the device. However, Lockridge et al. from the same or similar fields of endeavor disclose a system comprising timestamping element for placing a time stamp into the data stream when the data is received from a network, the time stamp and the packet are stored in a buffer prior to being provided to other devices (see paragraphs 27 and 38).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device.

The motivation for implementing a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device is that it enables synchronization of the system.

Borland and Lockridge et al. fail to disclose a system, wherein the device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network. Watkins et al. from the same or similar fields of endeavor disclose a device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network (see Figure 3 numerals 316, 318, and 320, column 8 lines 19-24, and column 12 lines 1-3, storing a signal on a recordable multimedia disk in the Fibre Channel network).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement the device includes the recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network as taught by Watkins et al. into the system of Borland and Lockridge et al. in order to increase the reliability of the network.

Regarding claim 34, Borland discloses a Fibre Channel network comprising:

a first port configured to at least one of transmit and receive a plurality of frames of data (see Fig 2 reference numeral 260R, RX input stage is configured to receive optical signals);

a second port configured to transmit a plurality of frames of data (It is inherent that there are more than one ports configured to transmit optical signals in a network);

a third port configured to at least one of transmitted and receive a plurality of frames of data (see Fig 2 reference numeral 260T, TX output stage is configured to transmit optical signals);

a fourth port configured to transmit a plurality of frames of data (It is inherent that there are more than one ports configured to transmit optical signals in a network);

a first network device having a first node coupled to the first port (see Fig 2 reference numeral 210R, the speaker 210R is coupled to the RX input stage);

at least one second network device having a second node coupled to the third port (see Fig 2 reference numeral 210T, the microphone 210T is coupled to the TX output stage); and

a system operatively coupled to and adapted to interface with the first and second nodes, the system including:

at least one input interface couplable to receive a plurality of frames of data (see Fig 2 reference numeral 225T, the selector 225T is configured to receive an optical signal to be processed by the transmitter, wherein the optical signal could be a plurality of data frames), the plurality of frames of data being at least one of transmitted from and received back in a node of a Fibre Channel network (see Fig 2 reference numeral 225T, the optical signal received by the selector 225T is transmitted from either a microphone 210T or modem port 212);

an output interface couplable to provide the received frames of data in pulse code modulation (PCM) formatted frames to a device (see column 5 lines 30-32, the TX

output stage transmit optical signals to base unit transceiver 120 through wireless PCM link).

Borland fails to disclose a system, wherein at least one of the input interface and the output interface is further configured to time tag the received plurality of frames of data prior to the providing of the frames of data to the device. However, Lockridge et al. from the same or similar fields of endeavor disclose a system comprising timestamping element for placing a time stamp into the data stream when the data is received from a network, the time stamp and the packet are stored in a buffer prior to being provided to other devices (see paragraphs 27 and 38).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device.

The motivation for implementing a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device is that it enables synchronization of the system.

Borland and Lockridge et al. fail to disclose a system, wherein the device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network. Watkins et al. from the same or similar fields of endeavor disclose a device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network (see Figure 3 numerals 316, 318, and 320, column 8 lines 19-24, and column 12 lines 1-3, storing a signal on a recordable multimedia disk in the Fibre Channel network).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement the device includes the recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network as taught by Watkins et al. into the system of Borland and Lockridge et al. in order to increase the reliability of the network.

regarding claims 2, 21, and 37, the input interface includes an optical connection couplable to the node of the Fibre Channel network (see Fig 2 reference numeral 225T, the selector 225T is configured to receive an optical signal to be processed by the transmitter, wherein the optical signal could be a plurality of data frames);

regarding claims 5, 24, and 40, the input interface is programmable to receive frames of data that are transmitted from the node to destination nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claims 6, 25, and 41, the input interface is programmable to receive frames of data that are received at the node from source nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claims 7, 26 and 42, the input interface is programmable to thin input frames of data that are transmitted from the node to destination nodes having predetermined addresses (see column 5 lines 46-53);

regarding claims 8, 27, and 43, the device includes a telemetry device (see column 5 lines 30-32, base unit transceiver);

regarding claims 9, 28, and 44, the telemetry device includes a real-time monitor (see column 5 lines, base unit transceiver monitors and communicates in real-time);

regarding claims 10 and 45, the output interface is configured to provide the received frames of data in pulse code modulation (PCM) formatted frames (see column 5 lines 60-63);

regarding claims 16 and 51, further comprising a processor coupled to control the input interface and the output interface (see Fig 2 reference numeral 225T and column 5 lines 46-53, the selector 225T switches the input between microphone and modem port);

regarding claims 17 and 52, the processor is configured to program the input interface to receive frames of data at the node from source nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claims 18 and 53, the processor is configured to program the input interface to receive frames of data transmitted from the node to destination nodes

having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claims 35 and 36, further comprising a first Fibre Channel switch that includes the first and second ports and a second Fibre Channel switch that includes the third and fourth ports (see column 5 lines 34-53).

9. Claims 3, 11-15, 22, 29-33, 38, 46-50 are rejected under 35 U.S.C. 103(a) as being unpatentable over Borland and Lockridge et al. in view of Watkins et al. as applied to claims 1, 20, and 34 above, and further in view of Tedenstig (US 6,307,859).

Regarding claims 3, 22, and 38, Borland and Lockridge et al. in view of Watkins et al. disclose all the subject matter of the claimed invention except the system, wherein the output interface is coupled to the input interface and configured to receive the plurality of frames of data from the input interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface. However, Tedenstig from the same or similar fields of endeavor discloses a system, wherein the PCM frames are filled with an empty message to maintain a transfer rate of 64 kbit/sec, but other transfer rates are also possible (see column 5 lines 41-45 and column 7 lines 41-67).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system, wherein the output interface is coupled to

the input interface and configured to receive the plurality of frames of data from the input interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface as taught by Tedenstig into the interface unit of Borland and Lockridge et al. in view of Watkins et al.

The motivation for implementing a system, wherein the output interface is coupled to the input interface and configured to receive the plurality of frames of data from the input interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface is that it increases the efficiency of the system.

Regarding claims 11, 29 and 46, Borland and Lockridge et al. in view of Watkins et al. disclose all the subject matter of the claimed invention except the step of timestamping the PCM frames. However, Tedenstig from the same or similar fields of endeavor discloses a system, wherein each PCM frames is divided into a number of time slots, where a certain time slot normally is used for transfer of signals in one channel (see column 7 lines 41-67).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system, wherein the PCM frames are time stamped as taught by Tedenstig into the interface unit of Borland and Lockridge et al. in view of Watkins et al.

The motivation for implementing a system, wherein the PCM frames are time stamped is that it increases efficiency of the system.

Regarding claims 12-15, 19, 30-33, 47-50, and 54, Borland and Lockridge et al. in view of Watkins et al. disclose all the subject matter of the claimed invention except the system, wherein the output interface is configured to fill the PCM frames with a fill word at approximately 10 milliseconds to maintain a substantially constant output frame rate when a frame of data is not available from the input interface. However, Tedenstig from the same or similar fields of endeavor discloses a system, wherein the PCM frames are filled with an empty message to maintain a transfer rate of 64 kbit/sec, but other transfer rates are also possible (see column 5 lines 41-45 and column 7 lines 41-67).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system, wherein the output interface is configured to fill the PCM frames with a fill word at approximately 10 milliseconds to maintain a substantially constant output frame rate when a frame of data is not available from the input interface as taught by Tedenstig into the interface unit of Borland and Lockridge et al. in view of Watkins et al.

The motivation for implementing a system, wherein the output interface is configured to fill the PCM frames with a fill word at approximately 10 milliseconds to maintain a substantially constant output frame rate when a frame of data is not available from the input interface is that it increases efficiency of the system.

10. Claims 55-60, 63-68, and 74-76 are rejected under 35 U.S.C. 103(a) as being unpatentable over Borland, Lockridge et al., and Watkins et al. in view of White, III (US 6,561,454).

Regarding claims 55 and 56, Borland discloses a plurality of avionics units networked with a Fibre Channel network, the network comprising:

a first port configured to at least one of transmit and receive a plurality of frames of data (see Fig 2 reference numeral 260R, RX input stage is configured to receive optical signals);

a second port configured to transmit a plurality of frames of data (It is inherent that there are more than one ports configured to transmit optical signals in a network);

a third port configured to at least one of transmitted and receive a plurality of frames of data (see Fig 2 reference numeral 260T, TX output stage is configured to transmit optical signals);

a fourth port configured to transmit a plurality of frames of data (It is inherent that there are more than one ports configured to transmit optical signals in a network);

a first network device having a first node coupled to the first port (see Fig 2 reference numeral 210R, the speaker 210R is coupled to the RX input stage);

at least one second network device having a second node coupled to the third port (see Fig 2 reference numeral 210T, the microphone 210T is coupled to the TX output stage); and

a system operatively coupled to and adapted to interface with the first and second nodes, the system including:

an input interface couplable to receive a plurality of frames of data (see Fig 2 reference numeral 225T, the selector 225T is configured to receive an optical signal to be processed by the transmitter, wherein the optical signal could be a plurality of data frames), the plurality of frames of data being at least one of transmitted from and received at the at least one node in the Fibre Channel network (see Fig 2 reference numeral 225T, the optical signal received by the selector 225T is transmitted from either a microphone 210T or modem port 212);

an output interface couplable to provide the received frames of data in pulse code modulation (PCM) formatted frames to a device (see column 5 lines 30-32, the TX output stage transmit optical signals to base unit transceiver 120 through wireless PCM link).

Borland fails to disclose a system, wherein at least one of the input interface and the output interface is further configured to time tag the received plurality of frames of data prior to the providing of the frames of data to the device. However, Lockridge et al. from the same or similar fields of endeavor disclose a system comprising timestamping element for placing a time stamp into the data stream when the data is received from a network, the time stamp and the packet are stored in a buffer prior to being provided to other devices (see paragraphs 27 and 38).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device.

The motivation for implementing a system comprising a time tagging element to time tag data frames prior to the providing of the data frames to the device is that it enables synchronization of the system.

Borland in view of Lockridge et al. do not disclose a fixed wing aircraft comprising: a fuselage; at least one engine; and lift generating surface. However, the invention of White, III discloses an aircraft comprising: a fuselage; at least one engine; lift generating surface; and a data network (see abstract).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a fixed wing aircraft comprising: a fuselage; at least one engine; and lift generating surface as taught by White, III and replacing the data network with the fibre channel interface unit of Borland in view of Lockridge et al.

The motivation for implementing an aircraft comprising: a fuselage; at least one engine; and lift generating surface is that it increases versatility of the system.

Borland and Lockridge et al. fail to disclose a system, wherein the device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network. Watkins et al. from the same or similar fields of endeavor disclose a device includes a recorder to record the frames of data transmitted and received at the at least one node in the Fibre Channel network (see Figure 3 numerals 316, 318, and 320, column 8 lines 19-24, and column 12 lines 1-3, storing a signal on a recordable multimedia disk in the Fibre Channel network).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement the device includes the recorder to record the frames

of data transmitted and received at the at least one node in the Fibre Channel network as taught by Watkins et al. into the system of Borland and Lockridge et al. in order to increase the reliability of the network.

Regarding claim 57, Borland, Lockridge et al., and Watkins et al. in view of White, III disclose all the subject matter of the claimed invention except the rotary wing aircraft. However, it is well known in the art to implement an interface unit in a rotary wing aircraft.

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a fibre channel interface unit as taught by Borland into a rotary wing aircraft.

The motivation for implementing a fibre channel interface unit into a rotary wing aircraft is that it increases the efficiency of the aircraft.

Regarding claims 58 and 59, Borland discloses a system further comprising a first Fibre Channel switch that includes the first and second ports and a second Fibre Channel switch that includes the third and fourth ports (see column 5 lines 34-53);

regarding claim 60, the input interface includes an optical connection couplable to the node of the Fibre Channel network (see Fig 2 reference numeral 225T, the selector 225T is configured to receive an optical signal to be processed by the transmitter, wherein the optical signal could be a plurality of data frames);

regarding claim 63, the input interface is programmable to receive frames of data that are transmitted from the node to destination nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be

processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claim 64, the input interface is programmable to receive frames of data that are received at the node from source nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claim 66, the device includes a telemetry device (see column 5 lines 30-32, base unit transceiver);

regarding claim 67, the telemetry device includes a real-time monitor (see column 5 lines, base unit transceiver monitors and communicates in real-time);

regarding claim 68, the output interface is configured to provide the received frames of data in pulse code modulation (PCM) formatted frames (see column 5 lines 60-63);

regarding claim 74, further comprising a processor coupled to control the input interface and the output interface (see Fig 2 reference numeral 225T and column 5 lines 46-53, the selector 225T switches the input between microphone and modem port);

regarding claim 75, the processor is configured to program the input interface to receive frames of data at the node from source nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port);

regarding claim 76, the processor is configured to program the input interface to receive frames of data transmitted from the node to destination nodes having predetermined addresses (see column 5 lines 46-53, the selector selects which data frames to be processed based on some kind of identification, which identifies the microphone and the modem port).

11. Claims 61, 69-73, and 77 are rejected under 35 U.S.C. 103(a) as being unpatentable over Borland, Lockridge et al., and Watkins et al. in view of White, III as applied to claim 55 above, and further in view of Tedenstig.

Regarding claim 61, Borland, Lockridge et al., and Watkins et al. in view of White, III disclose all the subject matter of the claimed invention except the system, wherein the output interface is coupled to the input interface and configured to receive the plurality of frames of data from the input interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface. However, Tedenstig from the same or similar fields of endeavor discloses a system, wherein the PCM frames are filled with an empty message to maintain a transfer rate of 64 kbit/sec, but other transfer rates are also possible (see column 5 lines 41-45 and column 7 lines 41-67).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system, wherein the output interface is coupled to the input interface and configured to receive the plurality of frames of data from the input

interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface as taught by Tedenstig into the interface unit of input interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface as taught by Tedenstig into the interface unit of Borland, Lockridge et al., and Watkins et al. in view of White, III.

The motivation for implementing a system, wherein the output interface-is coupled to the input interface and configured to receive the plurality of frames of data from the input interface, the output interface being configured to perform a low fill function such that, in a normal mode of operation, when fibre channel data is unavailable the low fill function maintains a transmission rate of the plurality of frames transmitted from the output interface is that it increases the efficiency of the system.

Regarding claim 69, Borland, Lockridge et al., and Watkins et al. disclose all the subject matter of the claimed invention except the step of timestamping the PCM frames. However, Tedenstig from the same or similar fields of endeavor discloses a system, wherein each PCM frames is divided into a number of time slots, where a certain time slot normally is used for transfer of signals in one channel (see column 7 lines 41-67).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system, wherein the PCM frames are time stamped as taught by Tedenstig into the interface unit of Borland, Lockridge et al., and Watkins et al.

The motivation for implementing a system, wherein the PCM frames are time stamped is that it increases efficiency of the system.

Regarding claims 70-73 and 77, Borland, Lockridge et al., and Watkins et al. disclose all the subject matter of the claimed invention except the system, wherein the output interface is configured to fill the PCM frames with a fill word at approximately 10 milliseconds to maintain a substantially constant output frame rate when a frame of data is not available from the input interface. However, Tedenstig from the same or similar fields of endeavor discloses a system, wherein the PCM frames are filled with an empty message to maintain a transfer rate of 64 kbit/sec, but other transfer rates are also possible (see column 5 lines 41-45 and column 7 lines 4.1-67).

Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to implement a system, wherein the output interface is configured to fill the PCM frames with a fill word at approximately 10 milliseconds to maintain a substantially constant output frame rate when a frame of data is not available from the input interface as taught by Tedenstig into the interface unit of Borland, Lockridge et al., and Watkins et al..

The motivation for implementing a system, wherein the output interface is configured to fill the PCM frames with a fill word at approximately 10 milliseconds to

maintain a substantially constant output frame rate when a frame of data is not available from the input interface is that it increases efficiency of the system.

Conclusion

12. **Examiner's Note**: Examiner has cited particular columns and line numbers in the references applied to the claims above for the convenience of the applicant. Although the specified citations are representative of the teachings of the art and are applied to specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant in preparing responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the Examiner.

In the case of amending the claimed invention, Applicant is respectfully requested to indicate the portion(s) of the specification which dictate(s) the structure relied on for proper interpretation and also to verify and ascertain the metes and bounds of the claimed invention.

13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to PAO SINKANTARAKORN whose telephone number is (571)270-1424. The examiner can normally be reached on Monday-Thursday 9:00am-3:00pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ricky Ngo can be reached on 571-272-3139. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Pao Sinkantarakorn/
Examiner, Art Unit 2416

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PS